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DATE:	September 14, 2006	PAGES:	<u>12 (twelve) w/cover</u>
TO:	Examiner Thao X. Le GAU 2814	PHONE:	(571) 272-1708
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Message

DOCKET No. : GS 160D1

Please see attached: Amendment

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Patent
10/712,810

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

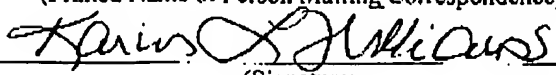
Applicant: Blanchard et al.
Serial No.: 10/712,810
Filed: November 13, 2003
Title: Method For Fabricating A Power Semiconductor Device Having A Voltage Sustaining Layer With A Terraced Trench Facilitating Formation Of Floating Islands
Art Unit: 2814
Examiner: Thao X. Le
Docket No.: GS 160 D1

Assistant Commissioner for Patents
Washington, D.C. 20231

RESPONSE TO OFFICE ACTION

Sir:

In response to the Office Action dated March 14, 2006, the time for response extending to September 14, 2006, kindly consider the following remarks. In addition, any deficiencies may be charged to deposit account No. 50-1047.

<p><u>CERTIFICATE OF FACSIMILE TRANSMISSION</u></p> <p>I hereby certify that this document and any document referenced herein has been transmitted via facsimile to the US Patent and Trademark Office at (571) 273-830 on <u>September 14, 2006</u>.</p> <p><u>Karin L. Williams, Reg. No. 36,721.</u> (Printed Name of Person Mailing Correspondence)</p> <p> (Signature)</p>
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